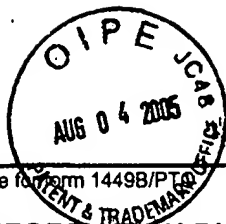



1



Substitute for Form 1449B/P		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	09/895,570
		Filing Date	6/28/2001
		First Named Inventor	Fabrizio Petrini
		Art Unit	2194
		Examiner Name	Qing Yuan Wu
		Attorney Docket Number	S-94,651
Sheet	2	of	2

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published (when known)	T ²
Q.W.		Diefendorff, "Compaq Chooses SMT for Alpha: Simultaneous Multithreading Exploits Instruction - and Thread-Level Parallelism," Microprocessor Report, Vol. 13, No. 16, December 1999, pp. 1-7.	
Q.W.		Keckler, et al., "Concurrent Event Handling through Multithreading," IEEECS, Vol. 48, No. 9, September 1999, pp. 903-916.	
Q.W.		Tseng et al., "All-to-All Personalized Communication in a Wormhole-Routed Torus," IEEECS, Vol. 7; No. 5, May 1996, pp. 490-505.	
Q.W.		Arpaci-Dusseau, et al., "Scheduling with Implicit Information in Distributed Systems," June 1998, pp. 1-11.	
Q.W.		Eggers, et al., "Simultaneous Multithreading: A Platform for Next-Generation Processors," IEEE Micro, September/October 1997, pp. 12-19.	
Examiner Signature			Date Considered 10/14/05

¹ Applicant's unique citation designation number (optional).

T² Applicant is to place a check mark here if English language Translation is attached.